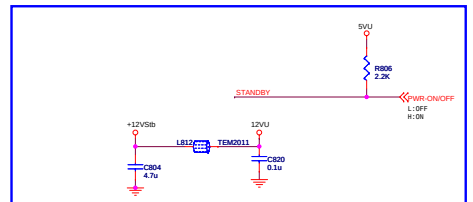
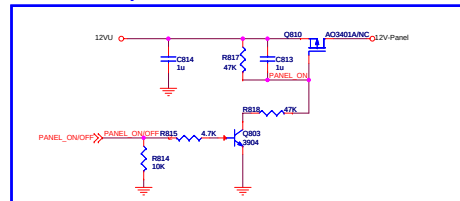


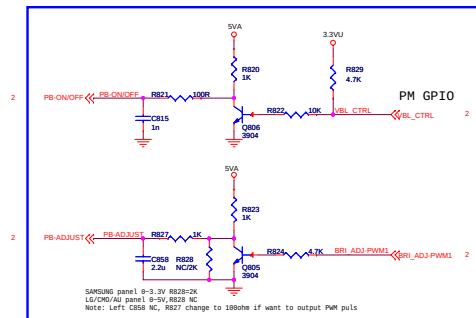
Connettor



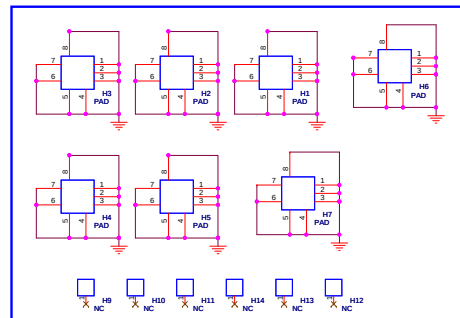
Power for panel



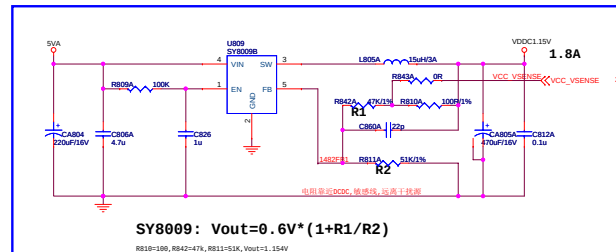
Inverter controller



Hole & Mark

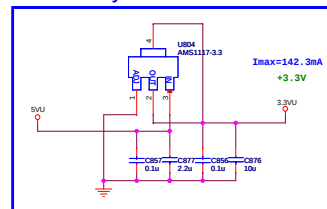


DC/DC

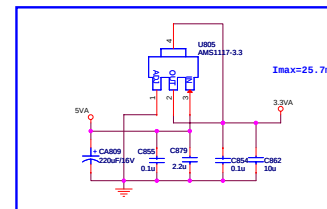


LDO

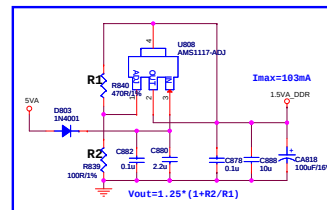
3.3V Standby Power



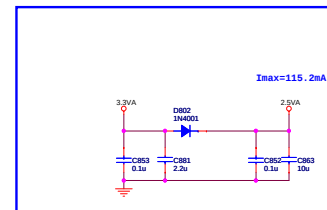
3.3V Normal Power



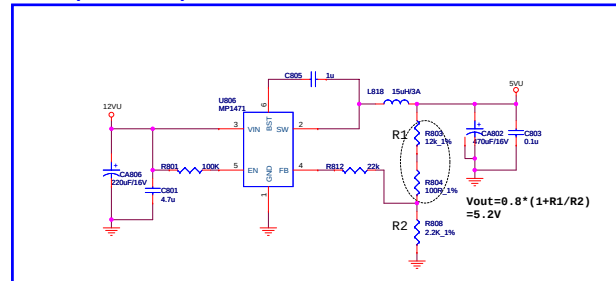
1.5V Normal Power for DDR3



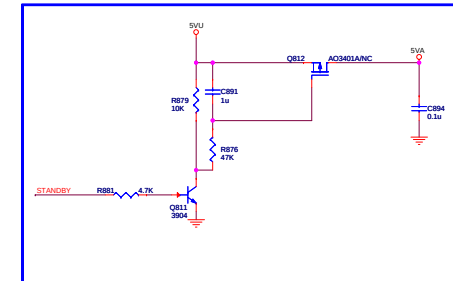
2.5V Normal Power for ADC

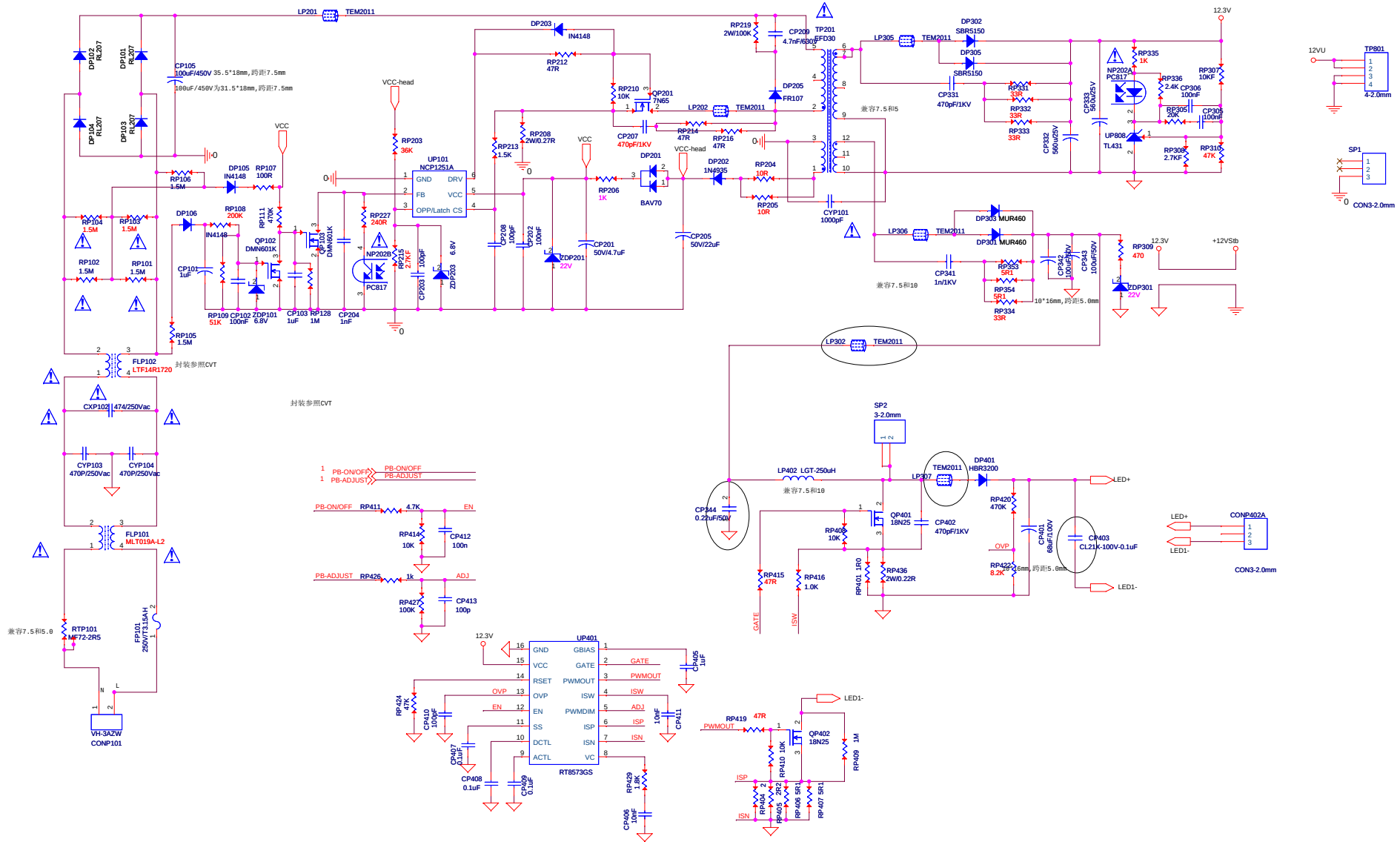


DC/DC (12V - - 5V)



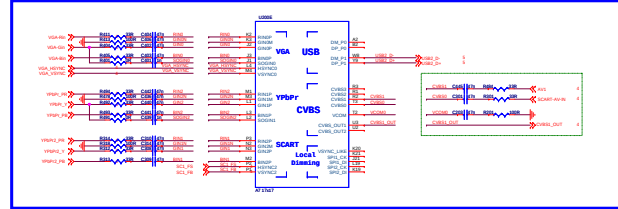
POWER SWITCH



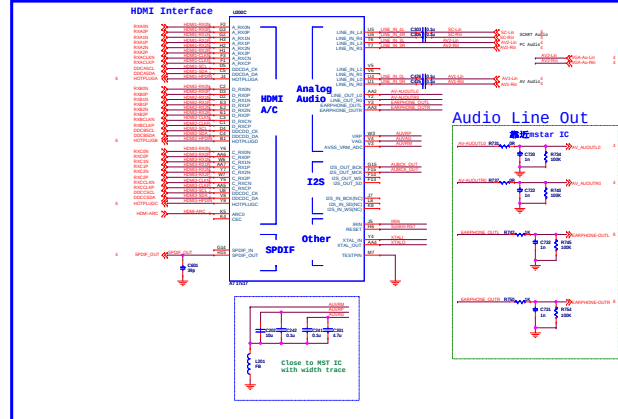


1. 此图仅供参考, 电路更改恕不预先通知;
2. 热地, 冷地
3. 凡有此标志元件, 其在整机安全和性能上有特殊要求, 请使用原规格型号。

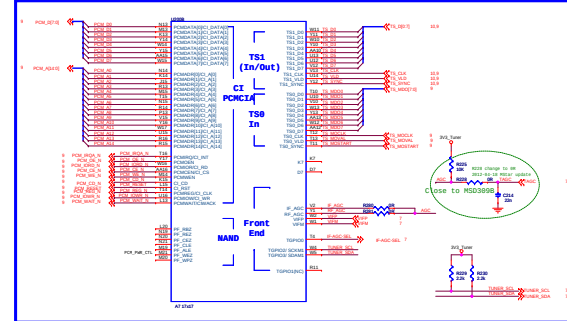
RGB & CVBS & USB



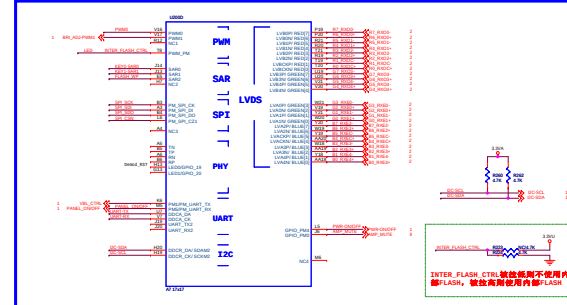
HDMI & Audio



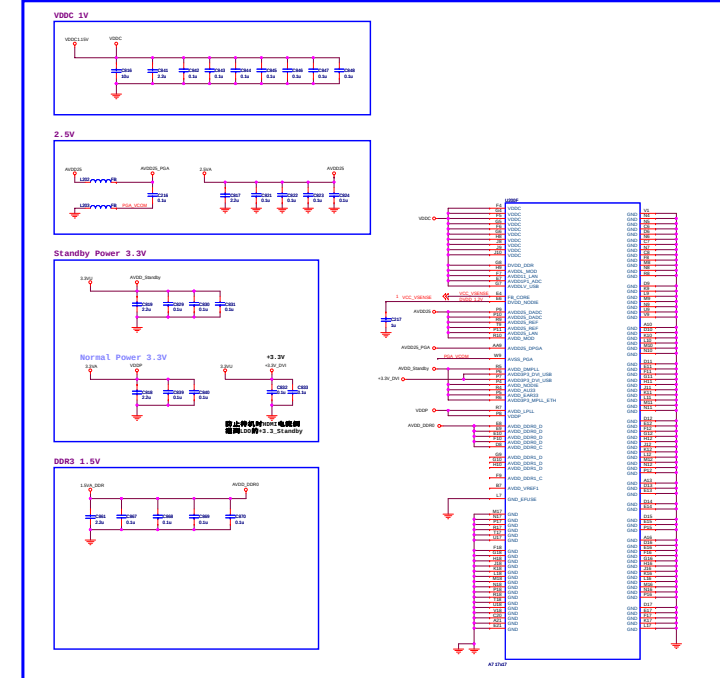
CI & TS & Front End



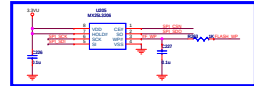
GPIO & LVDS



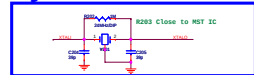
Power interface



FLASH



System XTAL



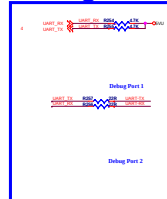
I2S-OUT



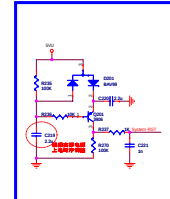
SPDIF_IN/3D_FLAG



Debug



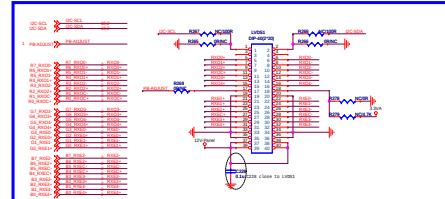
Reset



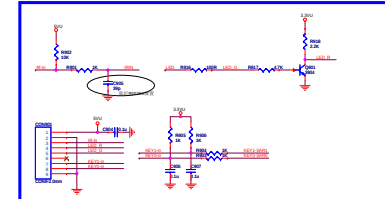
Mode Selection

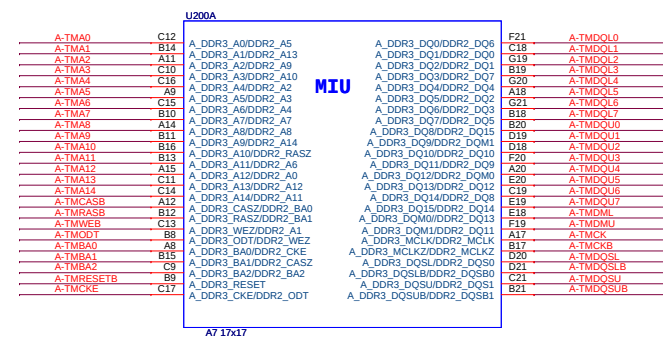
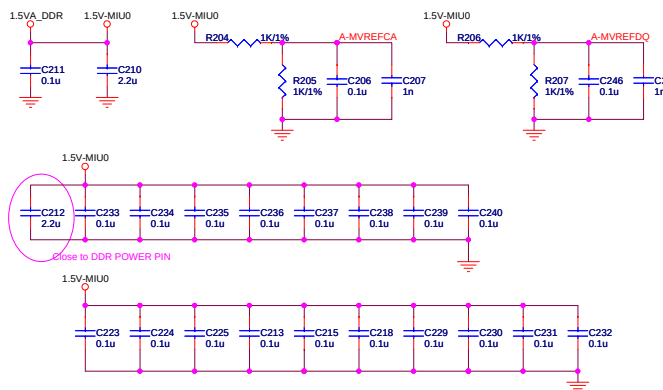


LVDS CONNECTOR

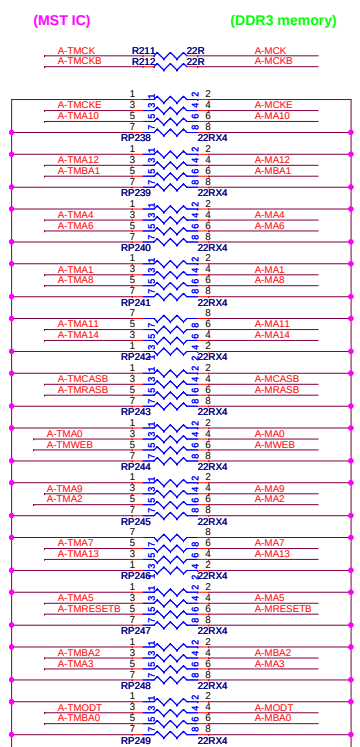
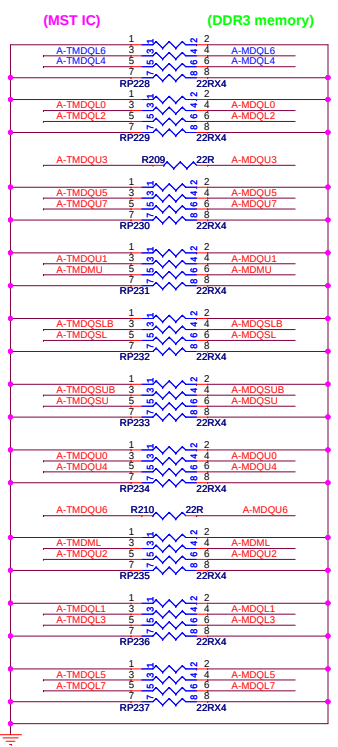
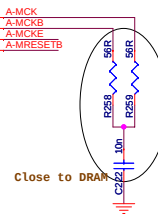
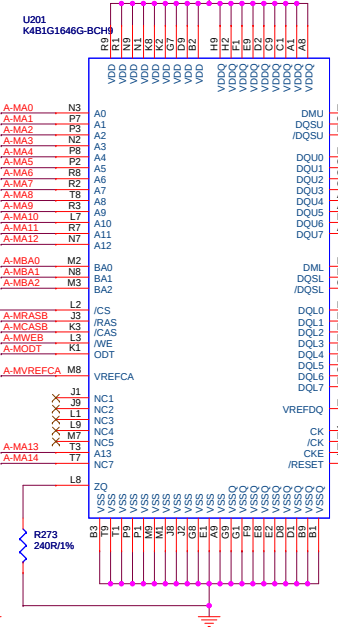


IR&KEYB

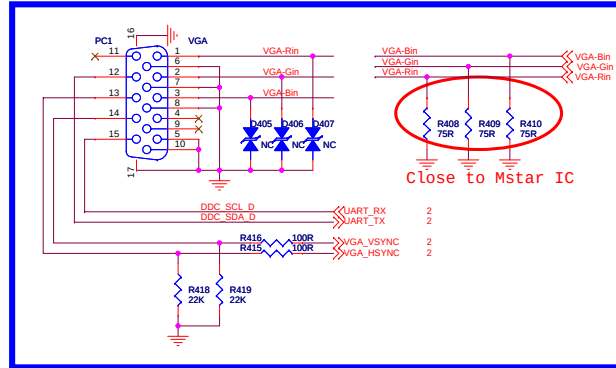




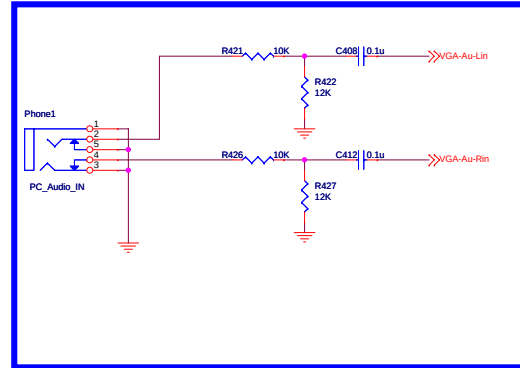
DDR3



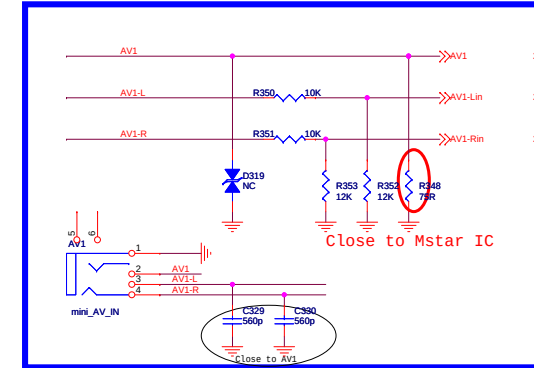
VGA INPUT



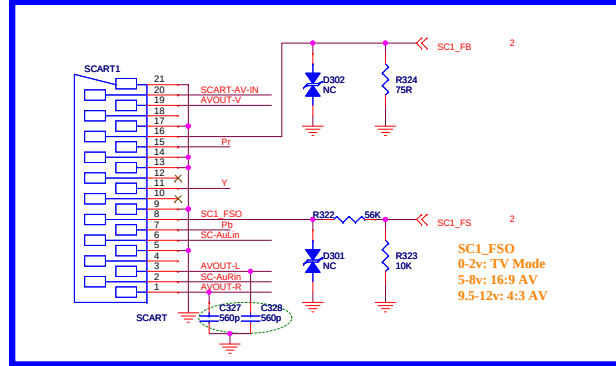
PC AUDIO IN



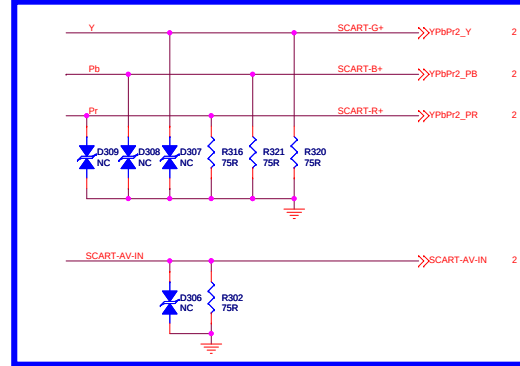
AV INPUT



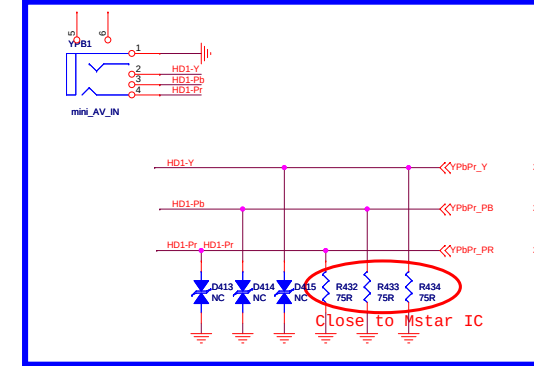
FULL SCART



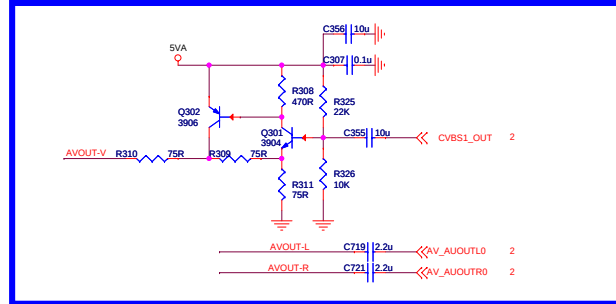
SCARTVIDEO IN



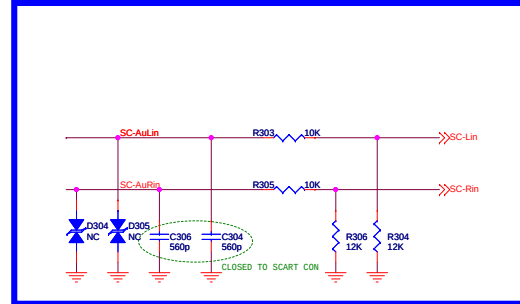
YPBPR INPUT



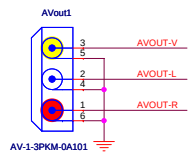
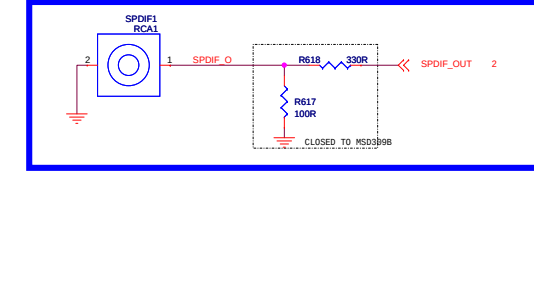
VIDEO OUT OP & AUDIO OUT



SCART AUDIO IN



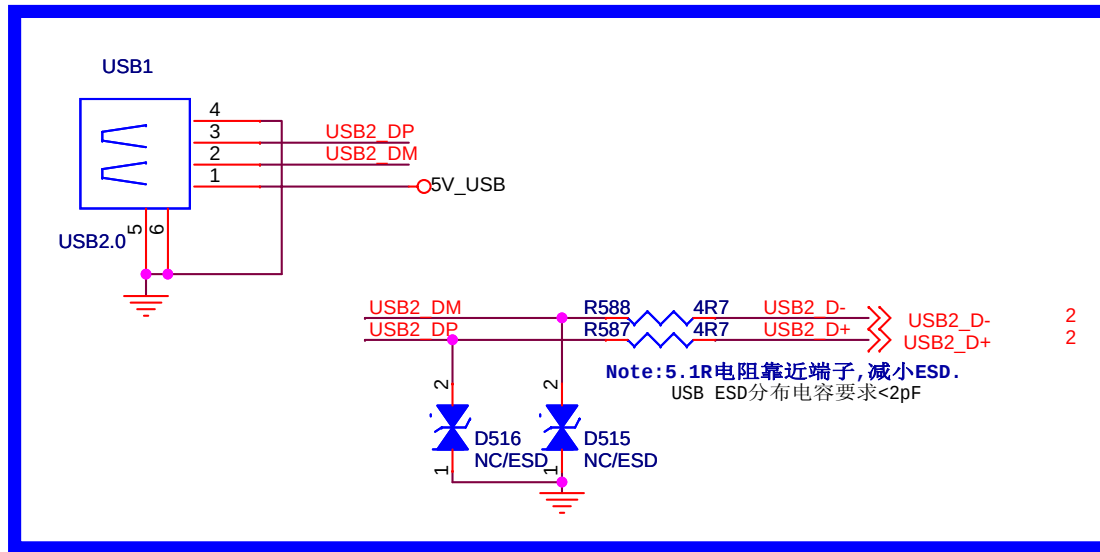
SPDIF OUTPUT



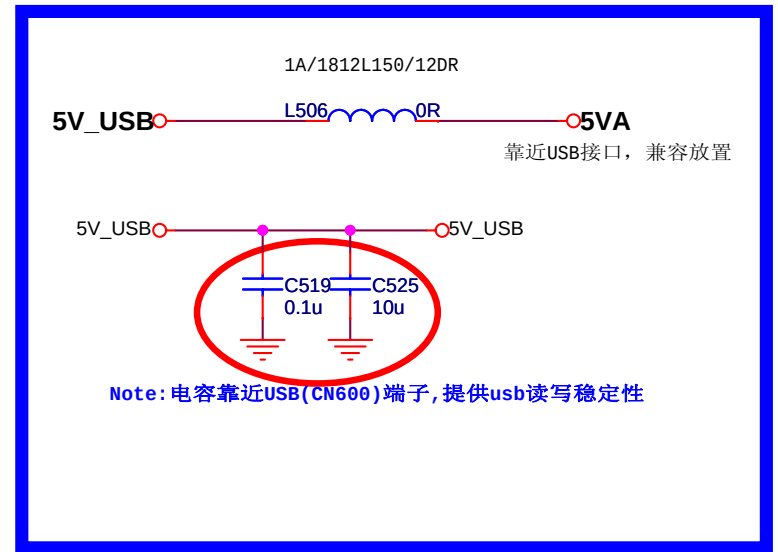
AV-1-3PKM-0A101

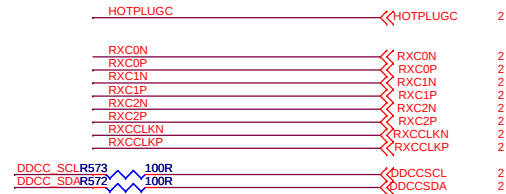
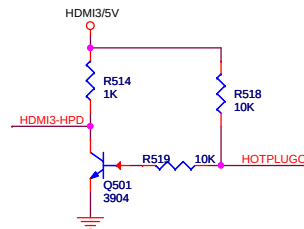
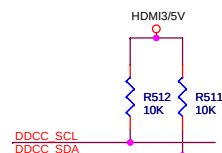
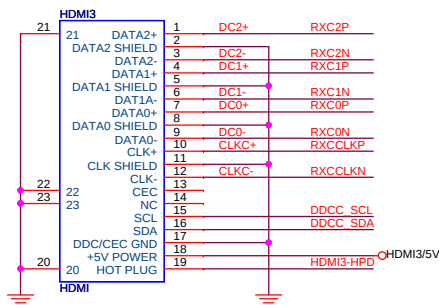
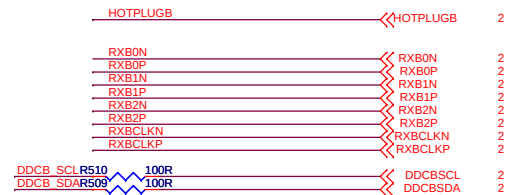
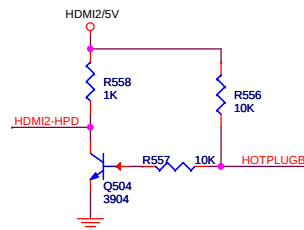
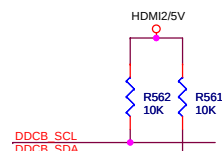
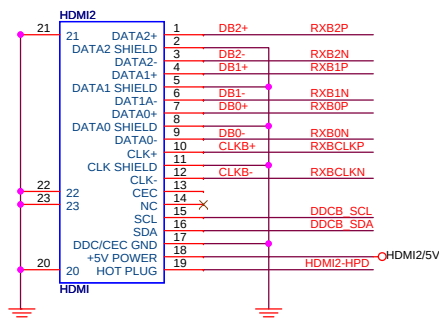
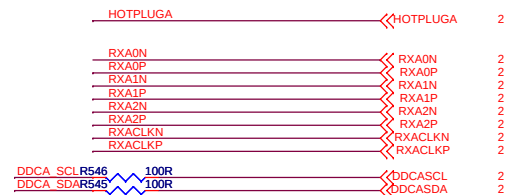
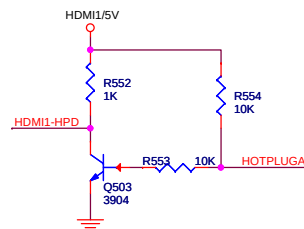
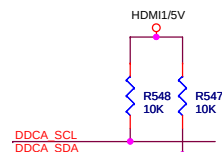
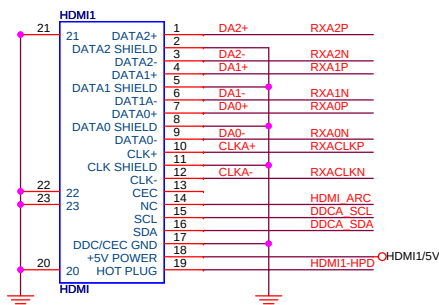
后视:
左: 红;
中: 白;
右: 黄;

USB Interface

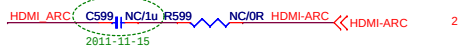


USB POWER

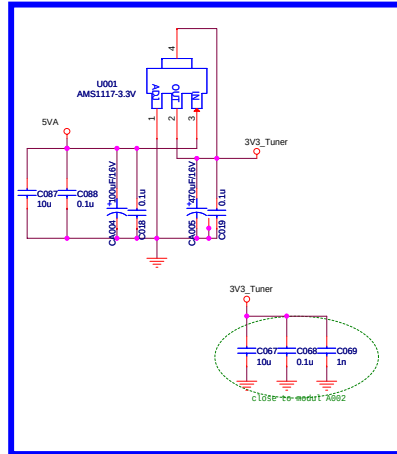




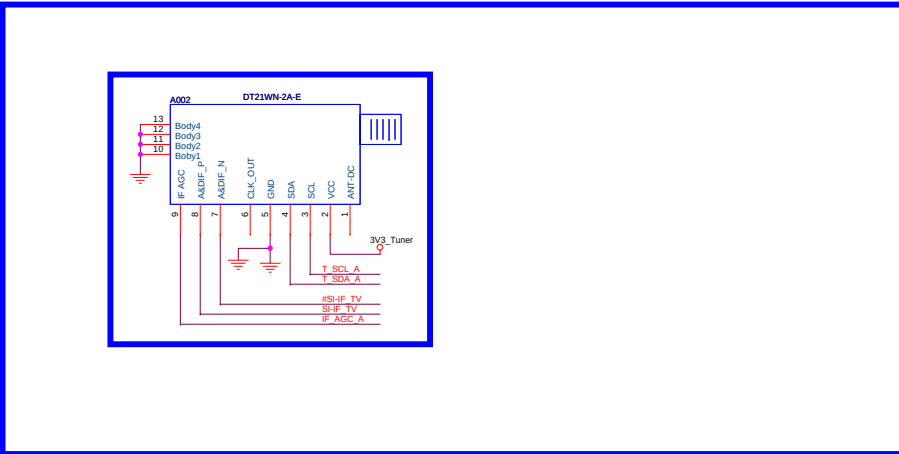
CEC & ARC



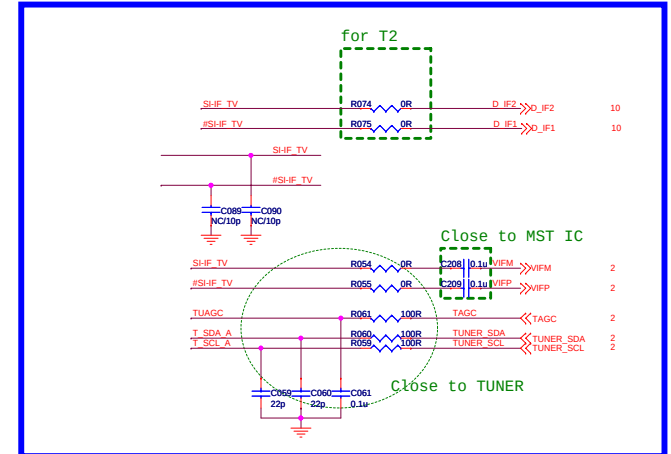
Tuner Power



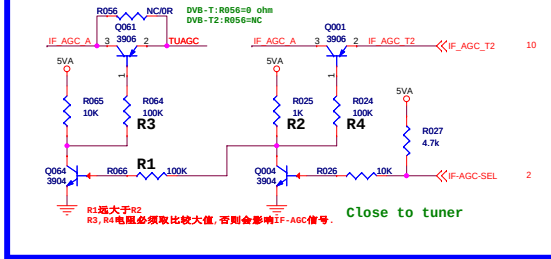
Silicon in can Tuner



IF AGC Switch



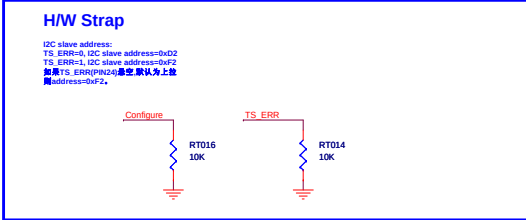
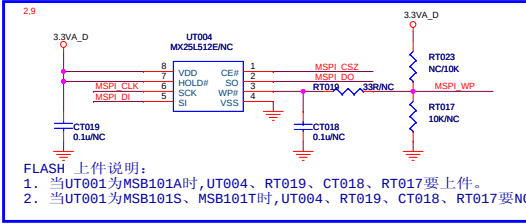
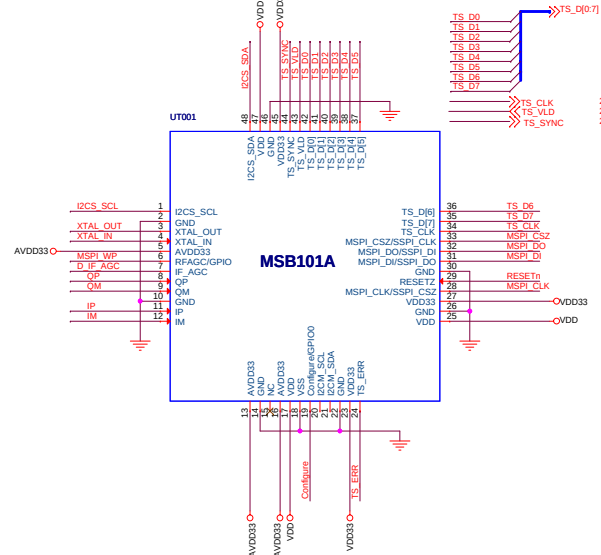
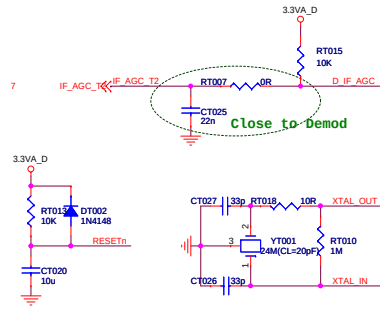
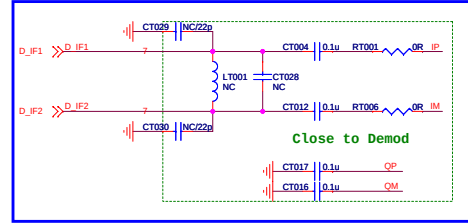
IF AGC Switch



[illegible]

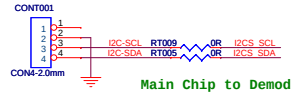
The diagram illustrates an AMP MUTE control circuit. It starts with a +12V input connected to a 1K resistor (R702) and a 0.47uF capacitor (C715) to ground. The signal then passes through a 10K resistor (R701) to the base of a 1N4148 diode (D701). The other end of D701 is connected to a 100K resistor (R706) and a 100uF/16V capacitor (CA712) to ground. The signal also passes through a 10K resistor (R775) to a 3.3VU input and an AMP_MUTE input. The output of the AMP_MUTE input is connected to the base of a 1N4148 diode (D702). The other end of D702 is connected to the base of a 1N4148 diode (D703). The other end of D703 is connected to the base of a 3904 transistor (Q701). The emitter of Q701 is connected to ground, and the collector is connected to a 10K resistor (R711) and a 47K resistor (R771) to ground. The output of the circuit is AMP_MUTE_CTR, which is connected to the collector of Q701 and a 47K resistor (R776) to ground. The circuit is powered by a +12V input connected to a 47K resistor (R778) to ground.

DVB-T/T2/C



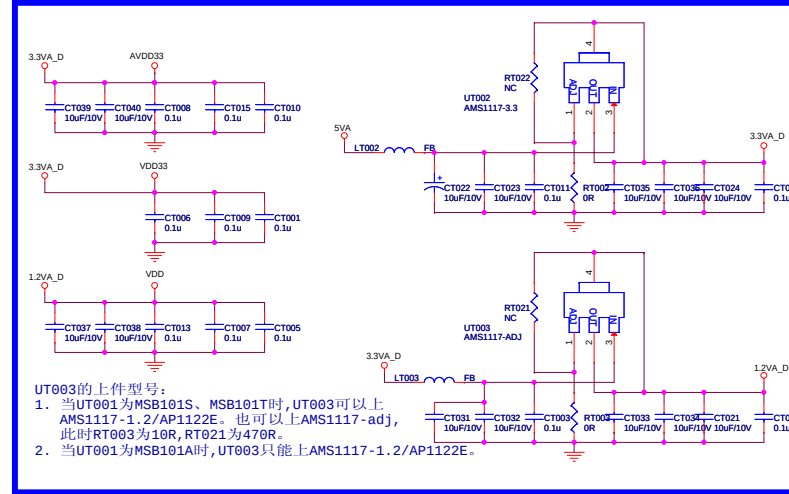
IZC-SDA IZC-SDA 2
IZC-SCL IZC-SCL 2

Debug port



Demod to Tuner

POWER



Edition Ifomation	
VER3	1, R054, R055封装更改为0402封装。 2, C217的封装更改为0603封装。 3, 增加贴片电容C884、C885、C886、C887、C888。 4, 删除贴片电容C088。 5, 删除插座CON201。
VER3	(only silicon in can)2012-2-21
	1,删除silicon on bord 部分电路（TDA18273及外围电路） 2, 兼容TCL及长虹Silicon in can modul 3, 为了通过S2A测试，增加贴片电容C329、C330 4, 增加贴片电阻R707 5, 增加贴片电阻R715、R717,贴片二极管D706、D707 6, 增加铝电解电容CA704
VER4	1,在VER3的基础上兼容3D屏,标准化LVDS插座！ 2, 删除了插座CON204

Title		
A7 17x17		
Size A	Document Number <Doc>	Rev <RevCode>
Date:	Friday, September 27, 2013	Sheet 17 of 17